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Very Large Scale Integration (VLSI) Systems, IEEE Transactions on , Volume: 6 , Issue: 4 , Dec. 1998  
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**2 On the delay-sensitivity of gate networks**

*Brzozowski, J.A.; Ebergen, J.C.*  
Computers, IEEE Transactions on , Volume: 41 , Issue: 11 , Nov. 1992  
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**3 DUDES: a fault abstraction and collapsing framework for asynchronous circuits**

*Shirvani, P.P.; Mitra, S.; Ebergen, J.C.; Roncken, M.*  
Advanced Research in Asynchronous Circuits and Systems, 2000. (ASYNC 2000) Proceedings. Sixth International Symposium on , 2-6 April 2000  
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**4 Optimizing CMOS Implementations of the C-element**

*Shams, M.; Ebergen, J.C.; Elmasry, M.I.*  
Computer Design: VLSI in Computers and Processors, 1997. ICCD '97. Proceedings., 1997 IEEE International Conference on , 12-15 Oct. 1997  
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*Shams, M.; Ebergen, J.C.; Elmasry, M.I.*  
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**6 Predicting performance of micropipelines using Charlie diagrams**

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**7 Delay-insensitive solutions to the committee problem**

*Benko, I.; Ebergen, J.C.;*

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Conference on ,Volume: 1 , 5-8 Jan 1993

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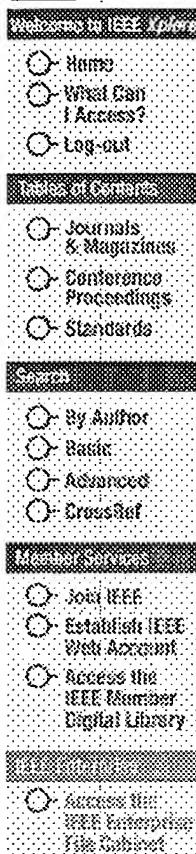


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Yan, Z.; Sarwate, D.V.;  
Computers, IEEE Transactions on, Volume: 52, Issue: 11, Nov. 2003  
Pages:1514 - 1519

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**2 High-speed easily testable Galois-field Inverter**  
Chih-Tsun Huang; Cheng-Wen Wu;  
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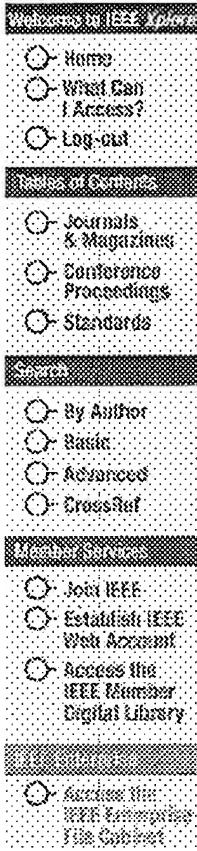
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